TOSHIBA Transistor Silicon NPN Triple Diffused Type

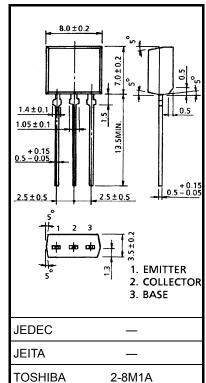
2SC5075

Switching Regulator and High-Voltage Switching Applications High-Speed DC-DC Converter Applications

- + High-speed switching: $t_{\rm r}$ = 1.0 μs (max), $t_{\rm f}$ = 1.0 μs (max)
- High breakdown voltage: $V_{CEO} = 400 \text{ V}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	500	V
Collector-emitter voltage	V _{CEO}	400	V
Emitter-base voltage	V _{EBO}	7	V
Collector current	Ι _C	2	А
Base current	Ι _Β	0.5	А
Collector power dissipation	P _C	1.3	W
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C



Weight: 0.55 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

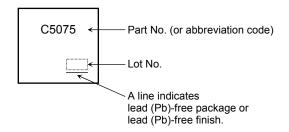
Industrial Applications

Unit: mm

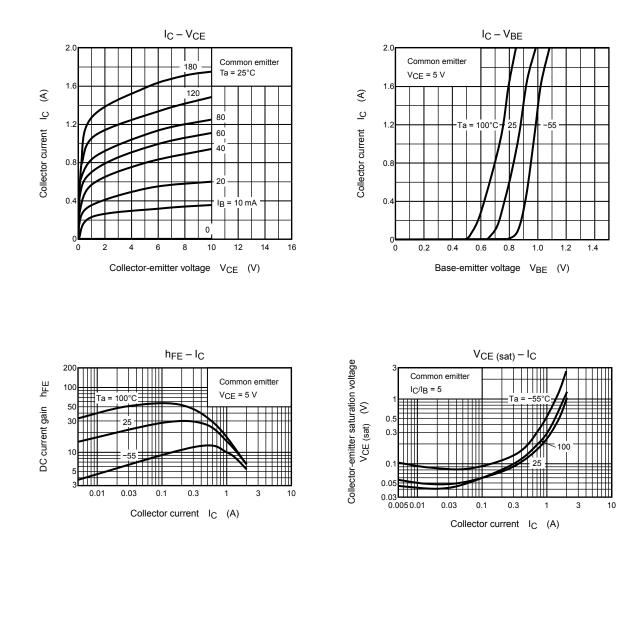
Electrical Characteristics (Ta = 25°C)

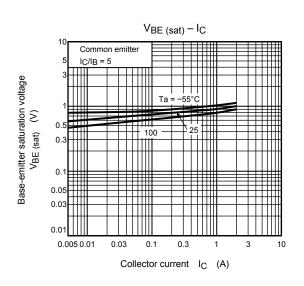
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off of	current	I _{CBO}	V _{CB} = 400 V, I _E = 0	_	_	100	μA
Emitter cut-off cu	rrent	I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	1	mA
Collector-base br	eakdown voltage	V (BR) CBO	I _C = 1 mA, I _E = 0	500	_	_	V
Collector-emitter	breakdown voltage	V (BR) CEO	I _C = 10 mA, I _B = 0	400	_	_	V
DC current gain		h _{FE}	V _{CE} = 5 V, I _C = 0.1 A	20	_	_	
			V _{CE} = 5 V, I _C = 1 A	8	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = 1 A, I _B = 0.2 A	_	_	1.0	V
Base-emitter saturation voltage		V _{BE (sat)}	I _C = 1 A, I _B = 0.2 A	_	_	1.5	V
Switching time	Rise time	t _{on}	$ \begin{array}{c} 20 \ \mu s \\ \hline \square put \\ \hline put \\ \hline \hline put \\$	_	_	1.0	μs
	Storage time	t _{stg}		_	_	2.5	
	Fall time	t _f		_	_	1.0	

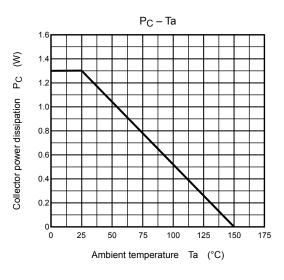
Marking

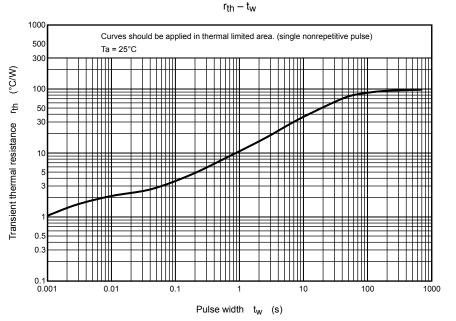


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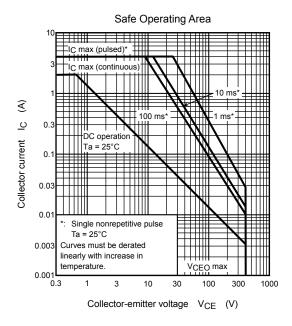








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RESTRICTIONS ON PRODUCT USE

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